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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10069522	FILING DATE 05/29/2002	CLASS 324	SUBCLASS 523	GAU 2862	EXAMINER LA.2 A.2
**APPLICANTS: Ishioka Shogo; Yamaoka Shuji; 285E					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/JP01/04994 06/13/2001 28					
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** FOREIGN APPLICATIONS VERIFIED: 28 JAPAN 2000-182118 06/16/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>Donald M. Fair</i>				ATTORNEY DOCKET NO 020239	
TITLE : Device and method for inspecting circuit board					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims <input type="checkbox"/> Print Claim for O.G. <input type="checkbox"/>	
Amount Due	Date Paid	DRAWING Sheets Drwg. <input type="checkbox"/> Figs. Drwg. <input type="checkbox"/> Print Fig. <input type="checkbox"/>	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		PREPARED FOR ISSUE Application Examiner	
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